

Dr. Ambedkar Institute of Technology
Department of Electrical and Electronics Engineering

The NAAC documents enclosed are verified and approved.

Jayaramulu G
HOD
Dept of EEE
Department of Electrical and Electronics Engg.
Dr. Ambedkar Institute of Technology
Bengaluru-560056
5/11/22

Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE
(An Autonomous Institution Affiliated to VTU, Belgaum)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of BOS Meeting

Agenda:

1. Finalizing the Scheme and Syllabus of UG for 2017-18 batch.
2. Reviewing the Scheme and Syllabus of existing batches.
3. Approval of Panel of examiners, Paper Setters, Practical Examination and Theory Valuation.
4. Finalizing the Scheme and Syllabus of PG (Power Electronics) for 2017-18 batch and to revise the Scheme and Syllabus of existing batches.

The meeting was convened on 25.03.2017 at 10.00 AM in the Research Laboratory. Chairman welcomed all the members of the Board of Studies in Electrical and Electronics Engineering. Chairman informed the members about-

- Preliminary BOS meetings were conducted on 15th, 18th & 20th March 2017, with student members of UG and PG programs and the course co-ordinators.
- A draft copy of scheme and syllabus was framed by considering their suggestions to make the courses more relevant.
- The members approved the scheme and syllabus with the following academic reforms.

RESOLUTIONS:

1. 3rd semester: Applicable to 2016- 2017 batch.
 - a. As per the regulation of Dr AIT (Autonomous), the total number of registered credits even after withdrawal of course shall be at least 20 (13.3.2- Academic regulation 2016-17). It was resolved to increase, the 3rd semester credits from 23 to 24, to allow the students to drop / withdraw any course. This has been resolved by increasing the credits of the core subject **EE32 Logic Design** from 3 to 4.
 - b. The contents of the core subject **EE32 Logic Design** were revised.

2. 5th semester: Applicable to 2015 batch and onwards.

- a. To maintain total credits of 200 (7.5- Academic regulation 2016-17), it was resolved to reduce 5th semester credits to 25 from 26. In view of this, core subject **EE52 Power Electronics - II** is reduced to 3 credits from 4.
- b. The contents of the core course subject **EE52 Power Electronics - II** were revised.

3. 6th semester: Applicable to 2015 batch and onwards.

It was resolved to shift Elective subject **Programmable Logic Controllers** from group-B of 5th semester to - group C of 6th semester, as this subject has advanced topics.

4. 7th semester: Applicable to 2016 batch and onwards.

- a. It was resolved to drop the course **EE737 Alternate Energy Sources** from elective group - E of 7th semester as most of the chapters of this subject are repeated in **Solar energy**, an Inter Department elective, offered by Mechanical Engineering Department, which most of our students register.
 - b. It was resolved to shift Elective subject **Electrical Power Quality** from group-B of 5th semester to - group E of 6th semester, as this subject has prerequisite courses in 6th semester.
5. Also members suggested to allot at least 2 credits to Project Phase-I in 7th semester.

Further the members authorized the chair person to make appropriate changes whenever required. Chairperson concluded the meeting with vote of thanks.



Dr. Jyoti P Koujalagi
Professor and Head
Dept. of Electrical Electronics Engg.
Dr. AIT, Bengaluru

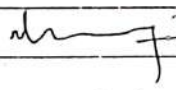
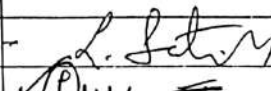
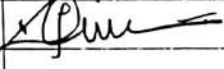
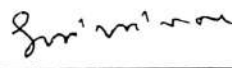
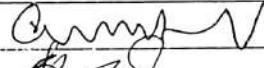
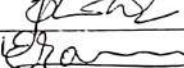
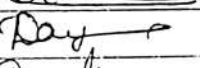

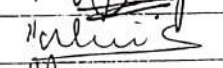
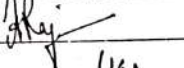
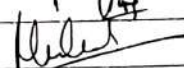
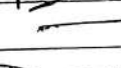

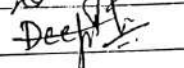
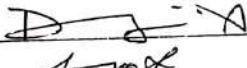
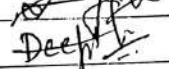

DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BENGALURU

(An Autonomous Institution Affiliated to VTU, Belgaum)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF BOARD OF STUDIES EEE MEMBERS FOR THE YEAR 2017-2019

Attendance List

S. No.	Names	Signatures
1	Dr. Jyoti P Koujalagi, Chairman	
2	Dr. Suryanarayana doolla, IIT, Mumbai	
3	Dr. Satish L, IISc, Bengaluru	
4	Dr. Pradeep Kumar Dixit, MSRIT, Bengaluru	
5	Dr. Saikumar-NIE, Mysuru	
6	Dr. Ravishankar Dixit , BMSEE, -VTU Nominee	
7	Dr. Balaram, PRDC, Bengaluru	
8	Mr. Chetan Rajdev, Deputy GM, Bosch Rexorth, Bengaluru	
9	Mr. Srikanth Kashyap, Director, JVS Electronics, Bengaluru	
10	Mr. Srinivas BT, Software Engineer, IBM India Pvt. Ltd, Bengaluru	
11	Dr. B.V. Sumangala, Professor	
12	Dr. Shankarlingappa.C.B Professor	
13	Mr. Eranna, Asso. Prof.	
14	Mr. Dayananda T.B, Asso. Prof.	
15	Mr. Govindaraju H.V, Asso. Prof.	
16	Mr. Vasudevamurthy S, Asso. Prof.	
17	Ms. Nalini S, Asso. Prof.	
18	Ms. Arpitha Raju	
19	Mr. Keshava Murthy, Visiting Professor	
20	Mr. MukundaSwamy, Asst. Prof.	
21	Mrs. Harini Vaikund, Asst. Prof.	
22	Mrs. Dhanyavathi., Asst. Prof.	
23	Mrs. Soumya S, Asst. Prof.	
24	Mrs. Deepti S.S, Asst. Prof.	

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of BOS meeting

Chairman welcomed all the members of the Board of Studies in Electrical and Electronics Engineering. The meeting was convened on 19.06.2018 at 10.00 a.m in Research Laboratory.

To make the courses more relevant, the BOS shall also do necessary changes and up gradation of all the courses/training programmes proposed by the Course Co-ordinators. In addition, BOS will also explore the possibilities for conducting few courses in collaborative manner with some other organizations. BOS may offer suggestions for Identification thrust areas for conduction of workshop /seminar/technical seminars during 11th Plan period. A pre-BOS meeting was conducted with Student Representatives.

Agenda:

1. Finalizing the Scheme and Syllabus of UG for 2018-19 batch.
2. Reviewing the Scheme and Syllabus of existing batches.
3. Approval of Panel of examiners, Paper Setters, Practical Examination and Theory Valuation.
4. Finalizing the Scheme and Syllabus of PG (Power Electronics) for 2018-19 batch and to revise the Scheme and Syllabus of existing batches.

Minutes:

- Chairperson gave introduction about the EEE Dept. and Academic Autonomy.
- The preliminary BOS meetings, along with students of UG and PG programs,. The proposals put forth by the students were examined and the relevant items were included in the draft syllabus to be approved by the members, BOS.
- The members unanimously approved the following academic reforms :

RESOLUTIONS: UG

The following changes are applicable to 2018 batch.

1. Two machine courses- Electrical Machines-I & Electrical Machines-II are mandatory for electrical course.
2. Board advised to make Modern control theory as an elective which was made as core in pre BOS meeting.
3. They advised to shift Generation, Transmission and Distribution course for higher semester.

4. Field theory course must be completed before doing Generation, Transmission and Distribution.
5. They mentioned to have theories and corresponding labs if any in same semester.
6. Management and humanitarian courses and/or credits should be reduced to include more number of core subjects.
7. They advised to reduce the credits of main project and include that in main subjects.
8. Classes not to be allotted for final year students on one or two days in a week to facilitate the students to carry out their project in industries.
9. Either the courses or credits need not be made compulsory but overall credits should be maintained as per the specified (175).
10. Operation & Research and PLC as core subjects to be included in the syllabus.

RESOLUTIONS: PG

1. Thesis to be given more weightage than internship as thesis plays a key role in placement for students unlike undergraduates.
2. In the first sem there should be an elective in place of miniproject / Industrial visit
3. Technical seminar is mandatory in the first semester only and has to be a credit course.
4. Second semester has to have a mini project which is evaluated by a committee of 4 to 5 members.
5. Final semester has to have only project and should not be clubbed with any other theory subjects.
6. Course can be clubbed with internship during third semester only.
7. Number of contact hours/week should be equal to the credits mentioned.

Further the members authorized the chair person to make appropriate changes whenever required. Chairperson concluded the meeting with vote of thanks.



Dr. Jyoti P Koujalagi
Professor and Head
Department of Electrical Electronics Engg.
Dr. AIT, Bengaluru



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF BOARD OF STUDIES EEE MEMBERS FOR THE YEAR 2017-2019

ATTENDANCE LIST - 19.06.2018

Sl.No	Name	Signature
1.	Dr. Jyoti P Koujalagi	
2.	Dr. Suryanarayana doolla, IIT Mumbai	
3.	Dr. Satish IISc, Bengaluru	
4.	Dr. Pradip Kumar Dixit, MSRIT, Bengaluru	
5.	Dr. Saikumar-NIE, Mysuru	
6.	Dr. Ravishankar Dixit, BMSCE, Bengaluru	 19.6.18
7.	Dr. Balaram, PRDC, Bengaluru	ABSENT
8.	Mr. Chetan Rajdev, Deputy GM, Bosch Rexorth, Bengaluru	
9.	Mr. Srikanth Kashyap, Director, JVS Electronics, Bengaluru	ABSENT
10.	Mr. Srinivas BT, Software Engineer, IBM India Pvt. Ltd, Bengaluru	ABSENT
11.	Dr. B.V. Sumangala, Professor	ABSENT
12.	Dr. Shankarlingappa.C.B Professor	
13.	Mr. Eranna, Asso. Prof.	
14.	Mr. Dayananda T.B, Asso. Prof.	
15.	Mr. Govindaraju H.V, Asso. Prof.	
16.	Mr. Vasudevamurthy S, Asso. Prof.	ABSENT
17.	Ms. Nalini S, Asso. Prof.	
18.	Ms. Arpitha Raju	
19.	Mr. Keshava Murthy, Visiting Professor	ABSENT
20.	Dr. Jayaramaiah G V, Professor	
21.	Mr. MukundaSwamy, Asst. Prof.	
22.	Mrs. Harini Vaikund, Asst. Prof.	
23.	Mrs. Dhanyavathi., Asst. Prof.	
24.	Mrs. Pankaja S, Asst. Prof.	
25.	Mrs. Deepti S.S, Asst. Prof.	

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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

Minutes of BOS meeting

Chairman welcomed all the members of the Board of Studies in Electrical and Electronics Engineering. The meeting was convened on 01.06.2019 at 10.00 a.m in Research Laboratory.

To make the courses more relevant, the BOS shall incorporate necessary changes and to upgrade the courses/training programmes proposed by the Course Coordinators. In addition, BOS will also explore the possibilities for conducting few courses in collaborative manner with industries. BOS may offer suggestions for identifying thrust areas for conduction of workshop /seminar/technical seminars during 11th Plan period. A pre-BOS meeting were conducted and suggestions from Student Representatives where incorporated.

Agenda:

1. Finalizing the Scheme of UG for 2018 and 2019 batch.
2. Reviewing the Syllabus of existing batches.
3. Approval of Panel of examiners, Paper Setters, Practical Examination and Theory Valuation.
4. Finalizing the Scheme and Syllabus of PG (Power Electronics) for 2019 and 2020 batch.

Minutes:

- Chairperson gave introduction about the EEE Dept. and Academic Autonomy.
- The preliminary BOS meetings, along with students of UG and PG programs were put forth before the members and the relevant suggestions were included in the draft syllabus to be approved by the members, BOS.
- The members unanimously approved the following academic reforms :

RESOLUTIONS: UG

The following are the suggestions given by the BOS members,


1. Dr. Puttaswamy PS suggested to retain the courses and reduce the credits keeping the contents intact at least for the 3rd and 4th semester.
2. Mr. Guruswamy expert from industry pointed out that an engineer should be able to replicate the basics what he/she has learnt effectively.
3. Focus should be good in basics or fundamental subjects for industry requirements.
4. Members mentioned to have theories and corresponding labs if any in same semester.
5. Mr. Ramachandra suggested to add Sensors and actuators to be one of the open elective.
6. Discussion on the curriculum design and the percentage of core and elective subjects were verified.

7. Power system planning which is at present in 6th semester should be shifted to higher semesters.
8. 3rd and 4th semester syllabus were discussed in detail.
9. Topic Saturation of Transformer to be include in the subject Transformer and Induction Machine.
10. Include self-study in all the modules of the courses; evaluation of such topics is not there for SEE.
11. Dr. Sathish and Dr. Sumathi S suggested not to have more than two books as textbooks, other books can be given as reference books.
12. Mr. Arun Kumar advised us to carry out simulation study for assignments of some subjects and the results can be submitted by the students. He also advised us to add simulation experiments along with the practical lab wherever possible.
13. The criteria for equivalence/credits for a particular subject has to be decided by the mentor coordinator with BOS coordinator & chairman.
14. The chairman of the BOS can decide as and when required the equivalent subjects to be offered to the backlog/readmission/change of college students.

RESOLUTIONS: PG

1. Mr. Guruswamy expert from industry pointed out that Snubber circuit to be included in SMPC (18EPE22) but it is already covered in PSDC (18EPE12).
2. Mr. Guruswamy also suggested that thermal design to be included in SMPC (18EPE22) but it is already covered in PSDC (18EPE12).
3. Mr. Ramachandra has suggested to incorporate Communication modules in DSP and applications.
4. Mr. Arun Kumar has insisted to include Grid connected inverters in uninterruptible power supply.
5. Dr. Puttaswamy PS advised us to add advance topics in each course as self-study component.
6. Mr. Arun Kumar has advised to include Programming in DSP course.

Further the members authorized the chair person to make appropriate changes whenever required. Chairperson concluded the meeting with vote of thanks.


Chairman, BOS
Dr. Jyoti P Koujalagi
Professor and Head
Department of Electrical Electronics Engg.
Dr. AIT, Bengaluru



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DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF BOARD OF STUDIES-EEE MEMBERS ATTENDED

Date: 1st June 2019

Sl.No	Name	Signature
1.	Dr. Jyoti P Koujalagi, Chair Person, BOE, EEE	
2.	Dr. Suryanarayana doolla, Professor, IIT Mumbai	
3.	Dr. Satish-IISc, Professor, Bengaluru	
4.	Dr. Pradip Kumar Dixit, Professor, MSRIT, Bengaluru	
5.	Dr. Puttaswamy PS, Professor, PESCE, Mandya	
6.	Dr. Sumathi, Professor, RNSIT, Bengaluru	
7.	Mr. Ramachandra, MD, Green Automation, Bengaluru	
8.	Mr. Arun Kumar, Director, Electrono Solutions, Bengaluru	
9.	Mr. Guruswamy, GM, IE Power technology, Bengaluru	
10.	Mr. Srinivas BT, Software Engineer, IBM, Bengaluru	
11.	Samarendra Prathap Singh, Asst. Prof, IET, Ayodhya, UP	
12.	Dr. B.V. Sumangala, Professor, Dr. AIT	
13.	Dr. Shankarlingappa. C.B, Professor, Dr. AIT	
14.	Dr. Eranna, Associate Professor, Dr. AIT	
15.	Mr. Dayananda T.B, Associate Professor, Dr. AIT	
16.	Dr. Govindaraju H.V, Associate Professor, Dr. AIT	
17.	Dr. Vasudevamurthy S, Associate Professor, Dr. AIT	
18.	Ms. Nalini S, Associate Professor, Dr. AIT	
19.	Ms. Arpitha Raju, Assistant Professor, Dr. AIT	
20.	Mr. Keshava Murthy, Visiting Professor, Dr. AIT	
21.	Mr. Mukunda Swamy, Assistant Professor, Dr. AIT	
22.	Mrs. Harini Vaikund, Assistant Professor, Dr. AIT	
23.	Mr. Rajesh LV, Asst. Prof.	



Department of Electrical & Electronics Engineering

Agenda 3: To frame, discuss and approve the syllabus of Basic Electrical Engineering and Basic Electrical Lab for the I/II Semester for the academic year 2020-21.

The Chairman presented the Syllabus of the Basic Electrical Engineering and Basic Electrical Lab for the I/II Semester students of all the branches of Engineering for the academic year 2020-21. Accordingly, a syllabus for the I/II Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the Syllabus of Basic Electrical Engineering and Basic Electrical Lab is discussed in length about the various aspects of the syllabus. After incorporation of the changes as suggested by the members of BOS, the **Basic Electrical Engineering and Basic Electrical Lab syllabus** for I/II Semesters was approved for the academic year 2020-21.

Agenda 4: To frame, discuss and approve the Scheme for III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2020-21.

The BOS Chairman presented the Scheme and Syllabus for the III and IV Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2020-21 and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes thoroughly for the III to IV Semester and discussed in length about various aspects of the scheme and syllabus. BoS members were suggested to incorporate the syllabus for virtual lab, inclusion of open source/Licensed software tools for the theory/laboratory. They also suggested to change in the syllabus with the mapping of Programme specific outcomes and course outcomes, the BOS members were approved the **Scheme and syllabus** for the III and IV Semesters for the academic year 2020-21 under Outcome Based Education System and Choice Based Credit System.

Agenda 5: To frame, discuss and approve the Scheme and Syllabus for V to VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2020-21 and syllabus of the Institute electives offered to the other department students of the college. The List of subjects for the Honours degree course were also placed before the committee members and also the rules and regulations of the Honour's degree were placed



Department of Electrical & Electronics Engineering

before the committee members.

Chairman presented the Scheme and Syllabus for V to VIII Semester for 4 Years Course Electrical and Electronics Engineering for the academic year 2020-21 and the list of subjects for the Honours degree from V semester onwards were placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes and Syllabus thoroughly for the V-VIII Semester applicable to 2018-2019 Batch and 2017-18 Batch respectively and discussed in length about various aspects of the syllabus of theory and laboratory. After incorporating the following subjects: Smart Grid Technology, and Electrical Vehicle subjects as suggested by the committee members of BOS the **Scheme and Syllabus** for V to VIII Semesters was approved for the academic year 2020-21. The committee have also approved the syllabus of the Institute/Open electives for the academic year 2020-21. The BOS members were approved the inclusion of subjects for the Honours degree courses from the V semester onwards as per the regulations laid down by the Dr AIT and VTU

Agenda 6: To prepare the panel for the Board of Examiners of the Electrical and Electronics Engineering to scrutinize the Question papers of the Semester End Examination for the AY: 2020-21

The BOS Chairman presented the list of Examiners to scrutinize the question papers for the Semester Examination for the academic year 2020-21

Resolution: The list of the Board of Examiners were approved for the AY 2020-21.

Agenda 7: To prepare the panel of Board of examiners for the evaluation and scrutinizing the Question papers in the department for the academic year 2020-21.

The Chairman of the BOS in E & EE department was presented the proposed the list panel of Examiners pertaining to UG in Electrical and Electronics Engineering department for the academic year 2020-21.

Resolution: The List of Panel of Examiners were approved based on the recommendation of internal members and the Chairman of BOS for the academic year 2020-21.

Agenda 8: To frame the list of subject equivalence for the academic year 2020-21.

The Chairman of the BOS in Electrical and Electronics Engineering presented the proposed subject equivalence pertaining to UG in Electrical and Electronics Engineering department for the academic year 2020-21.



Department of Electrical & Electronics Engineering

Resolution: The members of the BOS approved the subject equivalence for the students joined before the AY 2018-19, the BOS members were discussed in length about various aspects of the subject equivalence and approve the same.

Agenda 9: Inclusion of Virtual Lab for the academic year 2020-21.

The Chairman of the BOS in Electrical and Electronics Engineering presented the proposed subjects of Virtual Lab to UG in Electrical and Electronics Engineering for the academic year 2020-21.

Resolution: The members of the BOS approved the subjects of Virtual Lab for the academic year 2020-21 onwards after discussing in length about various aspects of the virtual lab. External BOS members expressed their opinion and appreciated the inclusion of the Virtual lab for UG courses

Agenda 10: Inclusion of Tools and Methods for ONLINE or Blended learning for the AY 2020-21

The External BOS members suggested to introduce, the Tools and Methods for ONLINE or Blended learning to UG in Electrical and Electronics Engineering for the academic year 2020-21.

Resolution: The members of the BOS approved the Tools for ONLINE/FLIPPED LEARNING ENVIRONMENT such as MICROSOFT TEAM, ZOOM, EDMODO, GOOGLE CLASS ROOM, EPILOGUE, CANVAS and CISCO WEBEX MEETING and also the members of BOS suggested to use the softwares like Sequel, Scilab, PSIM, Matlab, MiPower, PSCAD, Ansys, Open RTL for simulations for the academic year 2020-21 onwards. Also discussed in length about various aspects of the blended learning. External BOS members Expressed and apricated the inclusion of the blended learning for the UG courses

Finally, the meeting of Board of studies was concluded with the Chairman of the BOS in Electrical and Electronics Engineering thanking to all the members (Internal and External) of the BOS and Co-opted members for their active participation in the deliberations of the meeting and giving inputs for the progress of the department.

The following BOS members were presented for the BOS Meeting on Saturday, the 8th August 2020.

1. Dr.G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr..Sumathi, Professor and HOD, EEE, RNSIT, Bengaluru and VTU Nominee
3. Dr. Suryanarayanadoolla, Professor, Energy systems Engineering, IIT Bombay and Subject Expert
4. Dr. Pradeep Kumar Dixit, Professor and Head, Electrical Engineering, MSRIT, Bengaluru-19 and Subject Expert



Department of Electrical & Electronics Engineering

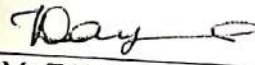
5. Dr.P.S. Puttaswamy, Professor and Head, Department of EEE, GSS, Mysore and Subject Expert
6. Professor Dilip Kumar , HOD EEE at IET Ayodhya, UP and Subject Expert
7. Mr.Srinivas.B.T.,Software Engineer, IBM India, Bengaluru and Post Graduate Student Nominee
8. Mr. Ramachandra, Green Automation Pvt. Ltd, Bengaluru and Industry Representative
9. Mr. Arun Kumar, Electrono Solution Pvt Ltd.Bengaluru and Industry Representative
10. Dr.Eranna, Associate Professor and BOS Member
11. Dr.Jyoti P Koujalagi, Professor and BOS Member
12. Mr.T.B.Dayananda, Associate Professor and BOS Member
13. Dr.H.V.Govindaraju, Associate Professor and BOS Member
14. Dr S.Vasudevamurthy, Associate Professor and BOS Member
15. Ms.Nalini. S, Assoicate Professor and BOS Member
16. Dr. Shankaralingappa.C.B., Professor and BOS Member
17. Ms Arpitha Raju, Assistant Professor and BOS Member

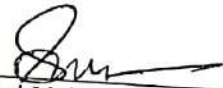
Members Absent:

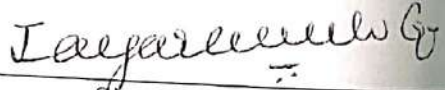
1. Mr. Guruswamy, General Manager, IE Power Technologies, Bengaluru and Industry Representative
2. Dr.Satish, Professor, IISc, Bengaluru and Subject Expert

Signature of Coordinators

1. 
Dr.Jyoti P Koujalagi

2. 
Mr.T.B.Dayananda

3. 
Harini Vaikund



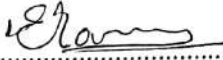
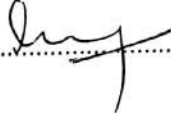



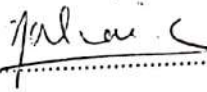
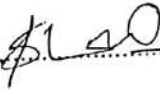
Signature of the BOS Chairman

Dr.G.V. Jayaramaiah



Dr. AMBEDKAR INSTITUTE OF TECHNOLOGY, BEGALURU- 560056.
(An Autonomous Institution Affiliated to Visvesvaraya Technological University, Belagavi)

Department of Electrical & Electronics Engineering

Name of the BOS Member	Signature of the BOS Member with date
1. Dr.Eranna, Associate Professor	
2. Dr.Jyoti P Koujalagi, Professor	
3. Mr.T.B.Dayananda	
4. Dr.H.V.Govindaraju, Associate Professor	
5. Dr.S.Vasudevamurthy, Associate Professor	
6. Ms.Nalini.S, Associate Professor	
7. Dr.Shankarlingappa.C.B., Professor	
8. Arpitha Raju	



Department of Electrical & Electronics Engineering

Minutes of the Board of Studies (BOS) Meeting:

The proceedings of the 12th Board of Studies (BOS) meeting of the Electrical and Electronics Engineering Department was held on Saturday, the 26th June 2021 at 11:00 AM through ONLINE MODE via Google Meet at EEE Department under the Chairmanship of Dr.G.V.Jayaramaiah, Professor and Head of the Department of Electrical and Electronics Engineering.

At the very outset, the Chairman welcomed all the Internal and External members of the BOS meeting through ONLINE MODE and gave a preliminary presentation on the agenda of approval of the scheme and syllabus of UG and PG for the academic year 2021-22

The chairman along with the BOS coordinator(s) gave a detailed presentation of the courses to be offered semester-wise: Professional core, Professional Elective, and Open Elective subjects to the UG level students. Also, the members were briefed about the Curriculum Design of the Department for the UG and PG courses.

The following agenda was placed by the Chairman which was discussed and resolved as follows:

Agenda 1: The confirmation of the proceedings of the 11th BOS meeting held on 08-08-2020

The 11th BOS meeting proceedings were confirmed.

Discussion: The members appreciated the progress during the academic year 2020-21.

Agenda 2: To frame, discuss and approve the Curriculum Design for the semesters I to VIII of UG for the Batch 2018-21 under Choice Based Credit System and Outcome Based Education System.

The Chairman appreciated the members about the introduction of Choice Based Credit System and Outcome Based Education System for a 4-year course in Electrical and Electronics Engineering with the course Matrix.

Accordingly, a draft Curriculum Design for the I to VIII Semesters was presented and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the Curriculum Design and discussed in length, the various aspects of the Curriculum Design. After incorporating, the changes suggested by the members of BOS, the Curriculum Design for I to VIII Semesters were approved for the AY 2018-21.



Department of Electrical & Electronics Engineering

Agenda 3: To frame, discuss and approve the Scheme for VII and VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2021-22.

The BOS Chairman presented the Scheme for the VII and VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2021-22 and placed before the Board Of Studies Members for their opinion and approval.

Resolution: The BOS members went through the draft Schemes thoroughly for the VII to VIII Semester and discussed in length, the various aspects of the scheme. BOS members were approved the same.

Agenda 4: To frame, discuss and approve the Syllabus for VII and VIII Semester under Choice Based Credit System and Outcome Based Education System for 4 Years Course in Electrical and Electronics Engineering for the academic year 2021-22 and syllabus of the Institute electives/Open Electives offered to the other department students of the Institute.

The BOS Chairman presented the Syllabus for VII and VIII Semester for 4 Years Course in Electrical and Electronics Engineering for the academic year 2021-22.

Resolution: The BOS members went through the draft Syllabus thoroughly for the VII and VIII Semester applicable to the 2018-2021 Batch and discussed in length, the various aspects of the syllabus of theory and laboratories. The BOS committee has also approved the syllabus of the departmental Electives and Institute/Open electives and appreciated the inclusion of the elective subjects like Sensors and Transducers, Energy Auditing & Demand Side Management, Computer Control of Electrical Drives for the academic year 2021-22.

Agenda 5: To prepare the panel for the Board of Examiners of the Electrical and Electronics Engineering to scrutinize the Question papers of the Semester End Examination for the AY: 2021-22

The BOS Chairman presented the list of Examiners to scrutinize the question papers for the Semester End Examination for the academic year 2021-22

Resolution: The list of the Board of Examiners was approved for the AY 2021-22.

Agenda 6 To frame, discuss and approve the Scheme and Syllabus for III and IV Semester PG under Choice Based Credit System and Outcome Based Education System for 2 Years Course in Power Electronics for the academic year 2021-22.



Department of Electrical & Electronics Engineering

The BOS Chairman presented the Scheme and Syllabus for the III and IV Semester PG under Choice Based Credit System and Outcome Based Education System for the academic year 2021-22 and placed it before the Board Of Studies Members for their opinion and approval

Resolution: The BOS members went through the draft Scheme and Syllabus for the III and IV Semester PG applicable to the 2020-2022 Batch and discussed in length, the various aspects of the scheme and syllabus of theory. The BOS committee has also approved the Scheme and Syllabus for the AY 2020-21.

Agenda 07: Inclusion of Tools and Methods for ONLINE or Blended learning for the AY 2021-22

The External BOS members suggested introducing, the Tools and Methods for ONLINE or Blended learning to UG and PG in Electrical and Electronics Engineering for the academic year 2021-22.

Resolution: The members of the BOS approved the Tools for ONLINE/FLIPPED LEARNING ENVIRONMENT such as MICROSOFT TEAM, ZOOM, EDMODO, GOOGLE CLASSROOM, EPILOGUE, CANVAS and CISCO WEBEX MEETING and also the members of BOS suggested using software like Sequel, Scilab, PSIM, Matlab, MiPower, PSCAD, Ansys, Open RTL for simulations for the academic year 2021-22 onwards. Also discussed in length about various aspects of blended learning. External BOS members Expressed and appreciated the inclusion of blended learning for the UG and PG courses

Finally, the meeting of the Board of studies was concluded with a Vote of Thanks by the Chairman of the BOS and BOS Coordinator(s) to all the members (Internal and External) of the BOS and Co-opted members for their active participation in the deliberations of the meeting and giving their inputs for the progress of the departmental academics.

The following BOS members were presented for the BOS meeting on Saturday, the 26th of June 2021.

1. Dr. G.V.Jayaramaiah, Professor and HOD, Chairman of BOS
2. Dr. Sumathi, Professor and HOD, EEE, RNSIT, Bengaluru, VTU Nominee
3. Dr. Suryanarayanadoolla, Professor in Energy Science and Engineering, IIT Bombay,
Subject Expert
4. Dr. Pradeep Kumar Dixit, Professor, and Head, Electrical Engineering, MSRIT, Bengaluru,
Subject Expert.
5. Dr. P.S. Puttaswamy, Professor and Head, Department of EEE, GSS, Mysore, Subject
Expert.
6. Professor Dilip Kumar, HOD EEE at IET Ayodhya, UP, Invitee, Mentee Institute
7. Mr. Srinivas. B.T., Software Engineer, IBM India, Bengaluru, Post Graduate Alumnus
Student Nominee



Department of Electrical & Electronics Engineering

8. Mr Ramachandra, Green Automation Pvt. Ltd, Bengaluru, Industry Representative
9. Mr. Arun Kumar, Electrono Solution Pvt Ltd. Bengaluru, Industry Representative
10. Mr. Guruswamy GM, IE Power technology, Bengaluru, Industry Representative
11. Dr. Eranna, Associate Professor and BOS Member
12. Dr. Jyoti P Koujalagi, Professor and BOS Member
13. Mr. T.B. Dayananda, Associate Professor, BOS Member
14. Dr. H.V . Govindaraju, Associate Professor, BOS Member
15. Dr. S.Vasudevamurthy, Associate Professor, BOS Member
16. Ms. Nalini. S, Associate Professor, BOS Member
17. Dr. Shankaralingappa. C.B, Professor, BOS Member
18. Ms. Arpitha Raju, Assistant Professor, BOS Member

Members Absent:

1. Dr.Satish, Professor, IISc, Bengaluru, Subject Expert

Signature of Coordinators

1. Mr T.B. Dayananda


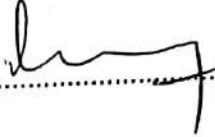
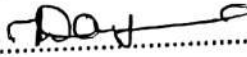

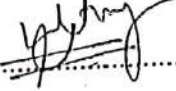



2. Mrs Harini Vaikund

Signature of the BOS Chairman

Dr. G.V. Jayaramaiah
Dr. Ambedkar Institute of Technology
Bengaluru - 560056



Department of Electrical & Electronics Engineering

Name of the BOS Member	Signature of the BOS Member with date
1. Dr Eranna, Associate Professor	
2. Dr Jyoti P Koujalagi, Professor	
3. Mr T.B Dayananda	
4. Dr H.V Govindaraju, Associate Professor	
5. Dr S Vasudevamurthy, Associate Professor	
6. Ms Nalini. S, Associate Professor	
7. Dr Shankarlingappa. C.B., Professor	
8. Ms Arpitha Raju, Assistant Professor	



DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE
(An Autonomous Institution Affiliated To VTU, Belgaum)
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING
III SEMESTER B.E., ACADEMIC YEAR (2018-19)
Batch - 2017

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
MA31	Engineering Mathematics-III	3	2	0	04	50	50	100
EE31	Analog Electronic Circuits	4	0	0	04	50	50	100
EE32	Logic Design	4	0	0	04	50	50	100
EE33	Network Analysis	3	2	0	04	50	50	100
EE34	Transformer and Induction Machines	4	0	0	04	50	50	100
EEL35	Electronic Circuits Lab	0	0	3	1.5	50	50	100
EEL36	Logic Design Lab	0	0	3	1.5	50	50	100
EEL37	Basic Electrical Lab	0	0	2	01	50	50	100
TOTAL		18	4	8	2.4	450	450	900

[Signature]
 CHAIR PERSON
 BOS, EEE


DEAN (Academic)

CHAIRMAN
 (Academic Council)

Dr. Jyoti P Koujalagi,	<i>[Signature]</i>	Mr. Srinivas BT	<i>[Signature]</i>
Dr. Suryanarayana	<i>[Signature]</i>	Dr. B.V. Sumangala	<i>[Signature]</i>
Dr. Satish L	<i>[Signature]</i>	Dr. Shankarlingappa.C.B	<i>[Signature]</i>
Dr. Pradeep Kumar Dixit	<i>[Signature]</i>	Mr. Eranna	<i>[Signature]</i>
Dr. Saikumar		Mr. Dayananda T.B	<i>[Signature]</i>
Dr. Ravi Shankar Dixit		Mr. Govindaraju H.V	<i>[Signature]</i>
Dr. Balaram		Mr. Vasudevamurthy S	<i>[Signature]</i>
Mr. Chetan Rajdev,		Ms. Nalini S	<i>[Signature]</i>
Mr. Srikanth Kashyap,		Ms. Arpitha Raju	<i>[Signature]</i>

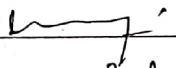
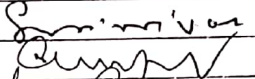
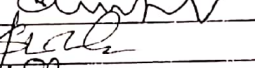
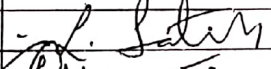
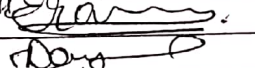
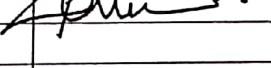
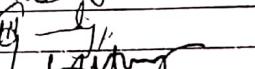
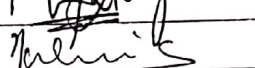




IV SEMESTER B.E, ACADEMIC YEAR (2018-19)
Batch - 2017

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
MA41	Engineering Mathematics-IV	3	2	0	04	50	50	100
EE41	Microcontrollers	3	0	0	03	50	50	100
EE42	Control Systems	3	2	0	04	50	50	100
EE43	Field Theory	4	0	0	04	50	50	100
EE44	Power Electronics-I	3	0	0	03	50	50	100
EE45	Generation, Transmission & Distribution	4	0	0	04	50	50	100
EEL46	Microcontrollers Lab	0	0	3	1.5	50	50	100
EEL47	Transformer & Induction Machines Lab	0	0	3	1.5	50	50	100
TOTAL		20	04	6	25	450	450	900


CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)


Dr. Jyoti P Koujalagi,		Mr. Srinivas BT	
Dr. Suryanarayana		Dr. B.V. Sumangala	
Dr. Satish L		Dr. Shankarlingappa.C.B	
Dr. Pradeep Kumar Dixit		Mr. Eranna	
Dr. Saikumar		Mr. Dayananda T.B	
Dr. Pav Shankar Dixit		Mr. Govindaraju H.V	
Dr. Balaram		Mr. Vasudevamurthy S	
Mr. Chetan Rajdev,		Ms. Nalini S	
Mr. Srikanth Kashyap,		Ms. Arpitha Raju	

V SEMESTER B.E, ACADEMIC YEAR (2019-20)
Batch - 2017

CODE NO.	COURSE	CONTACT HOURS/VEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
HS03	Management And Entrepreneurship	4	0	0	4	50	50	100
EE51	Signal & Systems	2	2	0	3	50	50	100
EE52	Power Electronics-II	3	0	0	3	50	50	100
EE53	DC Machines & Synchronous Machines	4	0	0	4	50	50	100
EE54x	Elective - A	3	0	0	3	50	50	100
EE55x	Elective - B	4	0	0	4	50	50	100
EEL56	Control Systems Lab	0	0	3	1.5	50	50	100
EEL57	Power Electronics Lab	0	0	3	1.5	50	50	100
EEL58	Simulation Lab	0	0	2	1	50	50	100
TOTAL		21	2	6	25	450	450	900

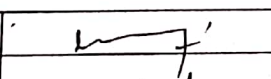
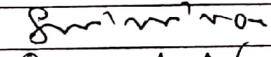

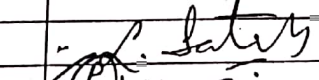

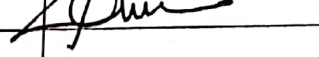
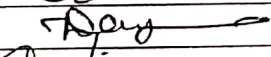
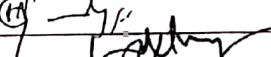




Elective- Group A (3 credits each)		Elective- Group B (4 credits each)	
EE541	Advanced Instrumentation System	EE551	VLSI Circuits & Design
EE542	Electrical Engineering materials	EE552	Operating System
EE543	Modern Control Theory	EE553	Linear IC's and Applications

Inter Department Electives : Students who have not completed the IDE should register for the completion of 200 credits. According to section 16.2, Academic Regulations of Dr AIT, the credits registered should not exceed 30.


CHAIR PERSON
BOS, EEE

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(Academic Council)

Dr. Jyoti P Koujalagi,		Mr. Srinivas BT	
Dr. Suryanarayana		Dr. B.V. Sumangala	
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Dr. Ravi Shanker Dixit		Mr. Govindaraju H.V	
Dr. Balaram		Mr. Vasudevamurthy S	
Mr. Chetan Rajdev,		Ms. Nalini S	
Mr. Srikanth Kashyap,		Ms. Arpitha Raju	

VI SEMESTER B.E, ACADEMIC YEAR (2019-20)
Batch – 2017

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
EE61	Power Systems Analysis	4	0	0	4	50	50	100
EE62	Electrical Machine Design	4	0	0	4	50	50	100
EE63	Digital Signal Processing	3	2	0	4	50	50	100
EE64X	Elective-C	3	0	0	3	50	50	100
EE65X	Elective- D	3	0	0	3	50	50	100
EEL66	DC Machines & Synchronous Machines Lab.	0	0	3	1.5	50	50	100
EEL67	Digital Signal Processing lab	0	0	3	1.5	50	50	100
EEP68	Mini Project	-	-	4	2	50	50	100
TOTAL		17	2	10	23	400	400	800

Students Shall Register For One Subject In Each Elective Group

Elective- Group C (3 credits each)		Elective- Group D (3 credits each)	
EE641	Electrical Power Utilization	EE651	Special Machines
EE642	Electrical Design, Estimating and Costing	EE652	Electrical Distribution Systems
EE643	Programmable Logic Controllers	EE653	Reactive Power Management

Inter Department Electives : Students who have not completed the IDE should register for the completion of 200 credits. According to section 16.2, Academic Regulations of Dr AIT, the credits registered should not exceed 30.

CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)

Dr. Jyoti P Koujalagi,		Mr. Srinivas BT	
Dr. Suryanarayana		Dr. B.V. Sumangala	
Dr. Satish L		Dr. Shankarlingappa.C.B	
Dr. Pradeep Kumar Dixit		Mr. Eranna	
Dr. Saikumar		Mr. Dayananda T.B	
Dr. Ravi Shankar Dixit		Mr. Govindaraju H.V	
Dr. Balaram		Mr. Vasudevamurthy S	
Mr. Chetan Rajdev,		Ms. Nalini S	
Mr. Srikanth Kashyap,		Ms. Arpitha Raju	

VII SEMESTER B.E, ACADEMIC YEAR (2020-21)

Batch – 2017

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
HS04	Intellectual Property Rights	2	0	0	2	25	25	50
EE71	Computer Techniques in Power System Analysis	3	2	0	4	50	50	100
EE72	High Voltage Engineering	4	0	0	4	50	50	100
EE73X	Elective – E	4	0	0	4	50	50	100
EEL74	Relay & HV Lab	0	0	3	1.5	50	50	100
EEL75	Power Systems Simulation Laboratory	0	0	3	1.5	50	50	100
EEL76	Electrical Drawing	1	0	4	3	50	50	100
EEP77	Project Work Phase-I	--	--	4	--	50	-	50
TOTAL		17	2	10	20	375	325	700

\$ -elective code of the department offering the course
 *Students shall register for a course offered by the other departments.

PROFESSIONAL ELECTIVE- GROUP E (4 credits each)

EE731	Flexible AC Transmission Systems(FACTS)	EE735	Fuzzy Logic
EE732	Energy Auditing & Demand Side Management	EE736	Artificial Neural Network
EE733	Power Systems Dynamics & Stability	EE737	Electrical Power Quality
EE734	Embedded Systems	EE738	Advanced Power Electronics*

Student shall register for one course in the elective group – E

Inter Department Electives : Students who have not completed the IDE should register for the completion of 200 credits. According to section 16.2, Academic Regulations of Dr AIT, the credits registered should not exceed 30.

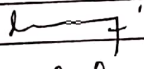
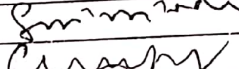
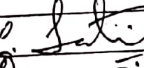
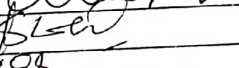
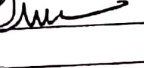
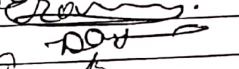
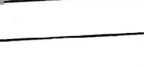








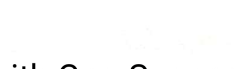


Inter - Departmental Electives offered by the Department

EEE01	Renewable Energy Sources	EEE02	Advanced Power Electronics
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 CHAIR PERSON
 BOS, EEE

DEAN (Academic)

CHAIRMAN
 (Academic Council)

Dr. Jyoti P Koujalagi,		Mr. Srinivas BT	
Dr. Suryanarayana		Dr. B.V. Sumangala	
Dr. Satish L		Dr. Shankarlingappa.C.B	
Dr. Pradeep Kumar Dixit		Mr. Eranna	
Dr. Saikumar		Mr. Dayananda T.B	
Dr. Ravi Shankar Dixit		Mr. Govindaraju H.V	
Dr. Balaram		Mr. Vasudevamurthy S	
Mr. Chetan Rajdev,		Ms. Nalini S	
Mr. Srikanth Kashyap,		Ms. Arpitha Raju	

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
EE81	Modern Power System Protection	4	-	0	4	50	50	100
EE82	Power System Operation & Control	2	2	0	3	50	50	100
EE83x	Elective - F	4	0	0	4	50	50	100
EEP84	Project Work Phase-II	--	--	09	12	100	200	300
EES85	Seminar/ Project Tour/Industrial Visit	--	--	--	2	50	-	50
TOTAL		13	2	09	25	300	350	650

ELECTIVE- GROUP F (4 credits each)

EE831	Testing & Commissioning of Electrical Equipment	EE835	Computer Control of Electrical Drives
EE832	HVDC Transmission	EE836	Electromagnetic Compatibility*
EE833	Insulation Engineering	EE837	Artificial intelligence applications to power systems
EE834	Advanced Control System*	EE838	Micro and Smart system technology

Student shall register for one course in the elective group - F

Inter Department Electives: Students who have not completed the IDE should register for the completion of 200 credits. According to section 16.2, Academic Regulations of Dr AIT, the credits registered should not exceed 30.

Inter - Departmental Electives offered by the Department

EEE03	Advanced Control System	EEE04	Electromagnetic Compatibility
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[Signature]
CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)

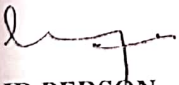
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Dr. Suryanarayana	<i>[Signature]</i>	Dr. B.V. Sumangala	<i>[Signature]</i>
Dr. Satish L	<i>[Signature]</i>	Dr. Shankarlingappa.C.B	<i>[Signature]</i>
Dr. Pradeep Kumar Dixit	<i>[Signature]</i>	Mr. Eranna	<i>[Signature]</i>
Dr. Saikumar	<i>[Signature]</i>	Mr. Dayananda T.B	<i>[Signature]</i>
Dr. Ravi Shankar Dixit	<i>[Signature]</i>	Mr. Govindaraju H.V	<i>[Signature]</i>
Dr. Balaram	<i>[Signature]</i>	Mr. Vasudevamurthy S	<i>[Signature]</i>
Mr. Chetan Rajdev,	<i>[Signature]</i>	Ms. Nalini S	<i>[Signature]</i>
Mr. Srikanth Kashyap,	<i>[Signature]</i>	Ms. Arpitha Raju	<i>[Signature]</i>



DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BANGALORE
(An Autonomous Institution Affiliated To VTU, Belgaum)
DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

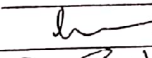
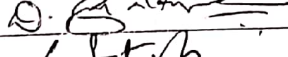

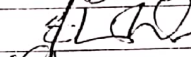
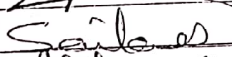
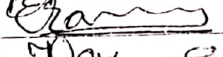
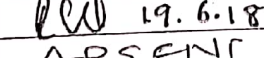
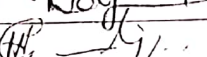
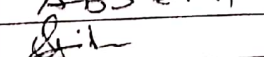
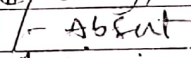
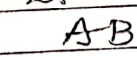
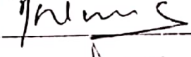

III SEMESTER B.E, ACADEMIC YEAR (2019-20)
Batch - 2018

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
3MAT31	Transforms & Complex Variable	2	2	0	03	50	50	100
3EE31	Analog Electronic Circuits	4	0	0	04	50	50	100
3EE32	Logic Design	4	0	0	04	50	50	100
3EE33	Microcontroller	2	2	0	04	50	50	100
3EE34	Generation, Transmission & Distribution	3	0	0	03	50	50	100
3EE35	Electrical Measurements	3	0	0	03	50	50	100
EEL36	Electronic Circuits Lab	0	0	2	1	50	50	100
EEL37	Logic Design Lab	0	0	2	1	50	50	100
KA38	Kannada/CIP	1	0	0	1	25	25	50
4ATDIP-1	(Dip)Advance Mathematics - II				0			
TOTAL		19	4	4	24	425	425	850


CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)

Dr. Jyoti P Koujalagi		Mr. Srinivas BT	ABSENT
Dr. Suryanarayana		Dr. B.V. Sumangala	ABSENT
Dr. Satish HSc		Dr. Shankarlingappa.C.B	
Dr. Pradip Kumar Dixit		Mr. Eranna	
Dr. Saikumar		Mr. Dayananda T.B	
Dr. Ravishankar Dixit	 19.6.18	Mr. Govindaraju H.V	
Dr. Balaram	ABSENT	Mr. Vasudevamurthy S	ABSENT
Mr. Chetan Rajdev		Ms. Nalini S	
Mr. Srikanth Kashyap	ABSENT	Ms. Arpitha Raju	

IV SEMESTER B.E, ACADEMIC YEAR (2019-20)

Batch - 2018

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
8MAT41	Numerical Methods and Probability Theory	2	2	0	03	50	50	100
18EE41	Control Systems	4	0	0	04	50	50	100
18EE42	Signal & Systems	2	2	0	03	50	50	100
18EE43	Power Electronics	3	0	0	03	50	50	100
18EE44	Network Analysis	3	2	0	04	50	50	100
18EE45	Analysis of Electrical Machines	3	2	0	04	50	50	100
8EEL46	Microcontrollers Lab	0	0	2	1	50	50	100
8EEL47	Power Electronics Lab	0	0	2	1	50	50	100
8HS402	Kannada/CIPH	1	0	0	1	25	25	50
8MATDIP-2	(Dip)Advance Mathematics - II				0			
	CIDIP-2				0			
	DIPENV-2				0			
TOTAL		18	06	4	24	425	425	850

[Signature]
CHAIR PERSON
 BOS, EEE

DEAN (Academic)


CHAIRMAN
 (Academic Council)

Dr. Jyoti P Koujalagi	<i>[Signature]</i>	Mr. Srinivas BT	ABSENT
Dr. Suryanarayana	<i>[Signature]</i>	Dr. B.V. Sumangala	ABSENT
Dr. Satish IISc	<i>[Signature]</i>	Dr. Shankarlingappa.C.B	<i>[Signature]</i>
Dr. Pradip Kumar Dixit	<i>[Signature]</i>	Mr. Eranna	<i>[Signature]</i>
Dr. Saikumar	<i>[Signature]</i>	Mr. Dayananda T.B	<i>[Signature]</i>
Dr. Ravishankar Dixit	<i>[Signature]</i>	Mr. Govindaraju H.V	<i>[Signature]</i>
Dr. Balaram	<i>[Signature]</i>	Mr. Vasudevamurthy S	ABSENT
Mr. Chetan Rajdev	<i>[Signature]</i>	Ms. Nalini S	<i>[Signature]</i>
Mr. Srikanth Kashyap	<i>[Signature]</i>	Ms. Arpitha Raju	<i>[Signature]</i>

V SEMESTER B.E, ACADEMIC YEAR (2020-21)




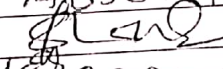

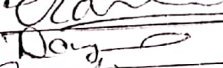

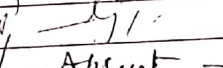
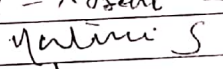



Batch - 2018

DE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
HS03	MC (M&E/IPR)	3	0	0	3	50	50	100
EE51	Digital Signal Processing	3	2	0	4	50	50	100
EE52	Field Theory	4	0	0	4	50	50	100
EE53	Power System Analysis	4	0	0	4	50	50	100
EE54	Linear IC's and Applications	4	0	0	4	50	50	100
EE55	Modern Control Theory	3	0	0	3	50	50	100
EEL56	Control Systems Lab	0	0	2	1	50	50	100
EEL57	Electrical Machines Lab	0	0	2	1	50	50	100
EE58	CIV	0	0	0	0	25	25	50
TOTAL		21	2	4	24	425	425	850


CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)

Dr. Jyoti P Koujalagi		Mr. Srinivas BT	ABSENT
Dr. Suryanarayana		Dr. B.V. Sumangala	ABSENT
Dr. Satish IISc		Dr. Shankarlingappa.C.B	
Dr. Pradip Kumar Dixit		Mr. Eranna	
Dr. Saikumar		Mr. Dayananda T.B	
Dr. Ravishankar Dixit	19.6.18	Mr. Govindaraju H.V	(H) 
Dr. Balaram	ABSENT	Mr. Vasudevamurthy S	- Absent -
Mr. Chetan Rajdev		Ms. Nalini S	
Mr. Srikanth Kashyap	ABSENT	Ms. Arpitha Raju	

VI SEMESTER B.E, ACADEMIC YEAR (2020-21)

Batch - 2018

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
18HS04	MC (M&E/IPR)	3	0	0	3	50	50	100
18EE61	Computer Techniques Power Systems Analysis	4	0	0	4	50	50	100
8EE62X	Elective-1	3	0	0	3	50	50	100
8EE63X	Elective- 2	3	0	0	3	50	50	100
8OEXX	Inter Department Elective	3	0	0	3	50	50	100
8EEL65	Digital Signal Processing lab	0	0	2	1	50	50	100
8EEL66	Power System Simulation Lab	0	0	2	1	50	50	100
8EEL67	Mini Project	-	-	2	3	50	50	100
	Internship	-	-	-	(2)	To be carried out during the intervening vacations of VI and VII semesters		
TOTAL		16	0	6	21	400	400	800

S -elective code of the department offering the course
*Students shall register for a course offered by the other departments.

Students Shall Register For One Subject In Each Elective Group

Elective- Group 1 (3 credits each)		Elective- Group 1 (3 credits each)	
18EE631	Electrical Power Utilization	18EE641	Power Systems Planning
18EE632	Electrical Design, Estimating and Costing	18EE642	Special Machines
18EE633	Programmable Logic Controllers	18EE643	FACTS Controllers
18EE634	OOPS with C++	18EE644	Research Methodology <i>Operational Research</i>

Inter Department Electives: Students who have not completed the IDE should register for the completion of 200 credits. According to section 16.2, Academic Regulations of Dr AIT, the credits registered should not exceed 30.

[Signature]
CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)

Dr. Jyoti P Koujalagi	<i>[Signature]</i>	Mr. Srinivas BT	ABSENT
Dr. Suryanarayana	<i>[Signature]</i>	Dr. B.V. Sumangala	ABSENT
Dr. Satish IISc	<i>[Signature]</i>	Dr. Shankarlingappa.C.B	<i>[Signature]</i>
Dr. Pradip Kumar Dixit	<i>[Signature]</i>	Mr. Eranna	<i>[Signature]</i>
Dr. Saikumar	<i>[Signature]</i>	Mr. Dayananda T.B	<i>[Signature]</i>
Dr. Ravishankar Dixit	<i>[Signature]</i>	Mr. Govindaraju H.V	<i>[Signature]</i>
Dr. Balaram	<i>[Signature]</i>	Mr. Vasudevamurthy S	absent
Mr. Chetan Rajdev	<i>[Signature]</i>	Ms. Nalini S	<i>[Signature]</i>
Mr. Srikanth Kashyap	ABSENT	Ms. Arpitha Raju	<i>[Signature]</i>

VII SEMESTER B.E, ACADEMIC YEAR (2021-22)

Batch - 2018

CODE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
8XX71	MC (E&C/OSHA)	3	0	0	3	50	50	100
8EE71	High Voltage Engineering	4	0	0	4	50	50	100
8EE74X	Elective -3	3	0	0	3	50	50	100
8EE75X	Elective -4	3	0	0	3	50	50	100
8OEXX	Inter Department Elective	3	0	0	3	50	50	100
8EEL76	Relay & HV Lab	0	0	2	1	50	50	100
8EEP78	Project Work Phase-I	--	--	2	2	50	50	100
TOTAL		16	0	4	19	500	500	1000

8 -elective code of the department offering the course
Students shall register for a course offered by the other departments.

Students Shall Register For One Subject In Each Elective Group

Elective- Group 3 (3 credits each)		Elective- Group 4 (3 credits each)	
18EE741	Electrical Drawing	18EE751	Insulation Engineering
18EE742	Energy Auditing And Demand Side Management	18EE752	Electromagnetic Compatibility
18EE743	Embedded Systems	18EE753	Advanced Power Electronics
18EE744	Power systems dynamics and stability	18EE754	Artificial Intelligence Applications to Power Systems

Inter Department Electives: Students who have not completed the IDE should register for the completion of 200 credits. According to section 16.2, Academic Regulations of Dr AIT, the credits registered should not exceed 30.

CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)

Dr. Jyoti P Koujalagi		Mr. Srinivas BT	ABSENT
Dr. Suryanarayana		Dr. B.V. Sumangala	ABSENT
Dr. Satish IISc		Dr. Shankarlingappa.C.B	
Dr. Pradip Kumar Dixit		Mr. Eranna	
Dr. Saikumar		Mr. Dayananda T.B	
Dr. Ravishankar Dixit		Mr. Govindaraju H.V	
Dr. Balaram	RECEIVED 19.6.18 ABSENT	Mr. Vasudevamurthy S	
Mr. Chetan Rajdev		Ms. Nalini S	
Mr. Srikanth Kashyap	ABSENT	Ms. Arpitha Raju	

VIII SEMESTER B.E, ACADEMIC YEAR (2021-22)

Batch - 2018

COURSE NO.	COURSE	CONTACT HOURS/WEEK			CREDITS	MAXIMUM MARKS		
		LECTURE	TUTORIAL	LAB		CONTINUOUS INTERNAL EVALUATION	SEMESTER END EVALUATION	TOTAL
EE81	MC (E&C/OSHA)	3	0	0	3	50	50	100
EE82	Modern Power System Protection	2	2	0	3	50	50	100
EE83x	Elective - 5	3	0	0	3	50	50	100
DEXX	Inter Department Elective*	3	0	0	3	50	50	100
EEP84	Project Work Phase-II	--	--	6	8	100	200	300
EES85	Seminar	--	--	--	1	25	-	25
INTXX	Internship	-	-	-	2	To be carried out during the intervening vacations of VI and VII semesters		
TOTAL		11	2	6	23	325	400	725

ELECTIVE- GROUP 5 (3 credits each)

EE831	Power System Operation & Control	18EE834	Computer Control of Electrical Drives
EE832	HVDC Transmission	18EE835	Micro and Smart System Design
EE833	Alternate Energy Sources	18EE836	Operating Systems

Student shall register for one course in the elective group - 5

Inter - Departmental Electives offered by the Department

EEEE01	Renewable Energy Sources	18EEE02	Electromagnetic Compatibility
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[Signature]
CHAIR PERSON
BOS, EEE

DEAN (Academic)

CHAIRMAN
(Academic Council)

Dr. Jyoti P Koujalagi	<i>[Signature]</i>	Mr. Srinivas BT	ABSENT
Dr. Suryanarayana	<i>[Signature]</i>	Dr. B.V. Sumangala	ABSENT
Dr. Satish IISc	<i>[Signature]</i>	Dr. Shankarlingappa.C.B	<i>[Signature]</i>
Dr. Pradip Kumar Dixit	<i>[Signature]</i>	Mr. Eranna	<i>[Signature]</i>
Dr. Saikumar	<i>[Signature]</i>	Mr. Dayananda T.B	<i>[Signature]</i>
Dr. Ravishankar Dixit	<i>[Signature]</i>	Mr. Govindaraju H.V	<i>[Signature]</i>
Dr. Balaram	REC 19.6.18 ABSENT	Mr. Vasudevamurthy S	- Absent -
Mr. Chetan Rajdev	<i>[Signature]</i>	Ms. Nalini S	<i>[Signature]</i>
Mr. Srikanth Kashyap	ABSENT	Ms. Arpitha Raju	<i>[Signature]</i>

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION for Batch: 2018, Academic Year: 2019-20
B.E Name of the programme: Electrical & Electronics Engineering
Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

III SEMESTER

Sl. No	Course and Course Code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	BC	18MA31	Mathematics (Title as per the decision of BoS in Sciences)	Maths	2	2	--	03	50	50	100	3
2	PC	18EE31	Analog Electronic Circuits		4	--	--	03	50	50	100	4
3	PC	18EE32	Logic Design		3	0	--	03	50	50	100	3
4	PC	18EE33	Network Analysis		3	2	--	03	50	50	100	4
5	PC	18EE34	Transformer and Induction Machines		4	0	--	03	50	50	100	4
6	PC	18EE35	Generation, Transmission & Distribution		3	0	--	03	50	50	100	3
7	PC	18EEL36	Electronic Circuits Lab		--		2	03	50	50	100	1
8	PC	18EEL37	Logic Design Lab		--		2	03	50	50	100	1
9	HS	18HS31/32	Constitution of India Professional Ethics and Human Rights// Env. Studies	Hu/Civ	1	--	--	02	50	50	100	1
10	MC	18HS33	Soft skills (MC)	Humanities	04		--	03	50	-	50	0
TOTAL					24	04	04	29	500	450	950	24

Course prescribed to lateral entry Diploma holders admitted to III semester of Engineering programs

11	M C	18MAD3 1	Advance Mathematics - I	Mathematics	02	01	--	03	50		50	0
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Sl.No	Name	Signature	Sl.No	Name	Signature
1	Dr. Jyoti P Koujalagi		11	Mr. Samarendra Prathap Singh	
2	Dr. Suryanarayana doolla		12	Dr. B.V. Sumangala,	
3	Dr. Satish L		13	Dr. Shankarlingappa.C.B	
4	Dr. Pradip Kumar Dixit		14	Dr. Eranna, Asso. Prof.	
5	Dr. Puttaswamy PS		15	Mr. Dayananda T.B,	
6	Dr. Sumathi, RNSIT		16	Dr. Govindaraju H.V,	
7	Mr. Ramachandra,		17	Dr. Vasudevamurthy S,	
8	Mr. Arun Kumar		18	Ms. Arpitha Raju	
9	Mr. Guruswamy		19	Mrs. Harini Vaikund	
10	Mr. Srinivas BT		20	Mr. V Keshavamurthy	

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
 SCHEME OF TEACHING AND EXAMINATION for Batch: 2018, Academic Year: 2019-20
 B.E Name of the programme: Electrical & Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

IV SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination			Credits	
					Theory Lecture	Tutorial	Practical Drawing	Duration in hours	CIE Marks	SEE Marks		Total Marks
					L	T	P					
1	BC	18MA41	Mathematics (Title as per the decision of BoS in Sciences)	Mathematics	2	2	--	03	50	0	100	3
2	PC	18EE41	Electrical & Electronic Measurements and Instruments		3	--	--	03	50	50	100	3
3	PC	18EE 42	Control Systems		4	--	--	03	50	50	100	4
4	PC	18EE 43	DC Machines and Synchronous Machines		4	0	--	03	50	50	100	4
5	PC	18EE 44	Power Electronics		4	0	--	03	50	50	100	4
6	PC	18EE 45	Linear Integrated Circuits & Applications		2	2	--	03	50	50	100	3
7	PC	18EE L46	Transformer and Induction Machines Lab		--		2	03	50	50	100	1
8	PC	18EE L47	Power Electronics Lab		--		2	03	50	50	100	1
9	HS	18HS41/42	Constitution of India Professional Ethics and Human Rights/ Env. Studies	Hum/Civ	1	--	--	02	50	50	100	1
10	M C	18HS43	Employability skills (MC)	Humanities	04		--	03	50	-	50	0
TOTAL					24	04	04	29	500	450	950	24

Course prescribed to lateral entry Diploma holders admitted to III semester of Engineering programs

11	MC	18MAD41	Advance Mathematics - II	Mathematics	02	01	--	03	50		50	0
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Sl.No	Name	Signature	Sl.No	Name	Signature
1	Dr. Jyoti P Koujalagi		11	Mr. Samarendra Prathap Singh	
2	Dr. Suryanarayana doolla		12	Dr. B.V. Sumangala,	
3	Dr. Satish L		13	Dr. Shankarlingappa.C.B	
4	Dr. Pradip Kumar Dixit		14	Dr. Eranna, Asso. Prof.	
5	Dr. Puttaswamy PS		15	Mr. Dayananda T.B,	
6	Dr. Sumathi, RNSIT		16	Dr. Govindaraju H.V,	
7	Mr. Ramachandra,		17	Dr. Vasudevamurthy S,	
8	Mr. Arun Kumar		18	Ms. Arpitha Raju	
9	Mr. Guruswamy		19	Mrs. Harini Vaikund	
10	Mr. Srinivas BT		20	Mr. V.Keshavamurthy	

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
 SCHEME OF TEACHING AND EXAMINATION for Batch: 2018, Academic Year: 2020-21
 B.E Name of the programme: Electrical & Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

V SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination			Credits	
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks		Total Marks
					L	T	P					
1	Hu	18HS51/52	M&E / IPR	Hu	3	--	--	03	50	50	100	3
2	PC	18EE 51	Signal and Systems		2	2	--	03	50	50	100	3
3	PC	18EE 52	Field Theory		4	--	--	03	50	50	100	4
4	PC	18EE 53	Microcontroller		4	--	--	03	50	50	100	4
4	PC	18EE 54	Electrical Machine Design		4	--	--	03	50	50	100	4
6	PE	18EE 55X	Professional Elective - 1		3	--	--	03	50	50	100	3
7	PC	18EE L57	Control System Lab		--		2	03	50	50	100	1
8	PC	18EE L58	DC Machines and Synchronous Machines Lab		--		2	03	50	50	100	1
9	PC	18EE L59	CAED Lab				2	03	50	50	100	1
TOTAL					20	2	4	24	400	400	800	24

18EE551	Programmable Logic Controllers	18EE553	Modern Control Theory
18EE552	VLSI Circuits & Design	18EE554	Embedded Systems

Sl. No	Name	Signature	Sl. No	Name	Signature
	Dr. Jyoti P Koujalagi		11	Mr. Samarendra Prathap Singh	
	Dr. Suryanarayana doolla		12	Dr. B.V. Sumangala,	
	Dr. Satish L		13	Dr. Shankarlingappa.C.B	
	Dr. Pradip Kumar Dixit		14	Dr. Eranna, Asso. Prof.	
	Dr. Puttaswamy PS		15	Mr. Dayananda T.B,	
	Dr. Sumathi, RNSIT		16	Dr. Govindaraju H.V,	
	Mr. Ramachandra,		17	Dr. Vasudevamurthy S,	
	Mr. Arun Kumar		18	Ms. Arpitha Raju	
	Mr. Guruswamy		19	Mrs. Harini Vaikund	
10	Mr. Srinivas BT		20	Mr. V. Keshavamurthy	

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION for Batch: 2018, Academic Year: 2020-21
B.E Name of the programme: Electrical & Electronics Engineering
Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

VI SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination			Credits		
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks		Total Marks	
					L	T	P						
1	Hu	18HS61/62	M&E/IPR	Hu	3	2	--	03	50	50	100	3	
2	PC	18 EE 61	Power System Analysis -I		4	--	--	03	50	50	100	4	
3	PC	18 EE 62	High Voltage Engineering		3	--	--	03	50	50	100	3	
4	PC	18 EE 63	Digital Signal Processing		3	2	--	03	50	50	100	4	
5	PE	18 EE 64X	Professional Elective - 2		3	--	--	03	50	50	100	3	
6	OE	18 OE 65X	Open Elective - A		3	--	--	03	50	50	100	3	
	PC	18 EE L66	Digital Signal Processing lab		--	--	2	03	50	50	100	1	
8	PC	18 EE L67	Microcontroller Lab		--	--	2	03	50	50	100	1	
9	MP	18EE MP68	Mini-project					03	50	50	100	3	
10	INT	18EE I69	Internship	(To be carried out during the intervening vacations of VI and VII semesters)				--	--	--	--	--	--
TOTAL					19	4	4	27	450	450	900	25	

Note: PC: Professional core, PE: Professional Elective, OE: Open Elective, MP: Mini-Project, INT: Internship.

Internship: All the students admitted to III year of BE have to undergo mandatory internship of 4 weeks during the vacations of VI and VII semesters and /or VII and VIII semesters. A University examination will be conducted during VIII semester and prescribed credit are added to VIII semester. Internship is considered as a head of passing and is considered for the award of degree. Those, who do not take-up/complete the internship will be declared as failed and have to complete during subsequent University examination after satisfy the internship requirements.

Electives

Open Elective Offered by the Department

Code	Professional Electives - 2	Code	Open Elective	Code	Open Elective
18 EE 641	Operating System	18 EEE 01	Renewable Energy Sources	18 EEE 04	Fuzzy Logic
18 EE 642	Special Machines	18 EEE 02	Electric Vehicles	18 EEE 05	Artificial Neural Network
18 EE 643	Power System Planning	18 EEE 03	Advanced Control System	18 EEE 06	Embedded System
18 EE 644	Electrical Power Utilization				

Sl.No	Name	Signature	Sl.No	Name	Signature
1	Dr. Jyoti P Koujalagi		11	Mr. Samarendra Prathap Singh	
2	Dr. Suryanarayana doolla		12	Dr. B.V. Sumangala,	
3	Dr. Satish L		13	Dr. Shankarlingappa.C.B	
4	Dr. Pradip Kumar Dixit		14	Dr. Eranna, Asso. Prof.	
5	Dr. Puttaswamy PS		15	Mr. Dayananda T.B,	
6	Dr. Sumathi, RNSIT		16	Dr. Govindaraju H.V,	
7	Mr. Ramachandra,		17	Dr. Vasudevamurthy S,	
8	Mr. Arun Kumar		18	Ms. Arpitha Raju	
9	Mr. Guruswamy		19	Mrs. Harini Vaikund	
10	Mr. Srinivas BT		20	Mr. V Keshava murthy	

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
 SCHEME OF TEACHING AND EXAMINATION for Batch: 2018, Academic Year: 2021-22
 B.E Name of the programme: Electrical & Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

VII SEMESTER

Sl No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination			Credits	
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks		Total Marks
					L	T	P					
1	MC	18HS71/72	CMEP / OSHA	IM/CV	2	--	--	03	50	50	100	2
2	PC	18 EE 71	Power System Analysis -II		4	--	--	03	50	50	100	4
3	PC	18 EE 72	Modern Power System Protection		3	--	--	03	50	50	100	3
4	PC	18 EE 73X	Professional Elective -3		3	--	--	03	50	50	100	3
5	PE	18 EE 74X	Professional Elective-4		3	--	--	03	50	50	100	3
6	PE	18 EE 75X	Open Elective -B		3	--	--	03	50	50	100	3
7	OE	18 EE 76	Power System Simulation Lab		--	--	2	03	50	50	100	1
8	PC	18 EE L76	Relay & HV Lab		--	--	2	03	50	50	100	1
9	Project	18 EE P78	Project Work Phase - 1		--	--	2	03	50	50	100	2
10	INT	18 EE I79	Internship		(If not completed after VI semester, it has to be carried out during the intervening vacations of VII and VIII semesters)							22
TOTAL					19	4	4	24	400	400	800	22

Note: PC: Professional Core, PE: Professional Elective, OE: Open Elective, INT: Internship, MC: Mandatory Course

Internship: All the students admitted to III year of BE have to undergo mandatory internship of 4 weeks during the vacations of VI and VII semesters and/or VII and VIII semesters. A SEE examination will be conducted during VIII semester and prescribed credits shall be added to VIII semester. Internship is considered as a head of passing and is considered for the award of degree. Those, who do not take-up/complete the internship will be declared as failed and have to complete during subsequent SEE examination after satisfy the internship requirements.

Electives		
Course code	Professional Electives - 3	Professional Electives - 4
18 EE 731	Fuzzy Logic	18 EE 741 Power System Operation & Control
18 EE 732	Artificial Intelligence Applications to Power Systems	18 EE 742 Computer Control of Electrical Drives
18 EE 733	Insulation Engineering	18 EE 743 Electrical Design, Estimating and Costing
18 EE 734	Flexible AC Transmission Systems (FACTS)	18 EE 744 Energy Auditing & Demand Side Management
18 EE 735	High Voltage Power Transformers	

Sl.No	Name	Signature	Sl.No	Name	Signature
1	Dr. Jyoti P Koujalagi		11	Mr. Samarendra Prathap Singh	
2	Dr. Suryanarayana doolla		12	Dr. B.V. Sumangala,	
3	Dr. Satish L		13	Dr. Shankarlingappa.C.B	
4	Dr. Pradip Kumar Dixit		14	Dr. Eranna, Asso. Prof.	
5	Dr. Puttaswamy PS		15	Mr. Dayananda T.B,	
6	Dr. Sumathi, RNSIT		16	Dr. Govindaraju H.V,	
7	Mr. Ramachandra,		17	Dr. Vasudevamurthy S,	
8	Mr. Arun Kumar		18	Ms. Arpitha Raju	
9	Mr. Guruswamy		19	Mrs. Harini Vaikund	
10	Mr. Srinivas BT		20	Mr. V. Keshavamurthy	

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION for Batch: 2018, Academic Year: 2021-2022
B.E Name of the programme: Electrical & Electronics Engineering
Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

VIII SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	MC	18XX81	CMEP / OSHA	IM /CV	4	--	--	03	50	50	100	2
2	OE	18XXE82	Open Elective -C		3	--	--	03	50	50	100	3
3	Project	18XXP83	Project Work Phase - 2		--	--	2	03	50	50	100	8
4	Seminar	18XXS84	Technical Seminar		--	--	2	03	50	50	100	1
5	INT	18XXI85	Internship	(Completed during the intervening vacations of VI and VII semesters and /or VII and VIII semesters.)				03	50	50	100	2
TOTAL					7	--	4	15	250	250	500	16

Note: PC: Professional Core, PE: Professional Elective, OE: Open Elective, INT: Internship, MC: Mandatory Course

Students can select any one of the open electives (Please refer to consolidated list of Dr. AIT open electives) offered by any Department.

Selection of an open elective is not allowed provided,

- The candidate has studied the same course during the previous semesters of the programme.
- The syllabus content of open elective is similar to that of Departmental core courses or professional electives.

Registration to electives shall be documented under the guidance of Programme Coordinator/ Mentor.

Internship: Those, who have not pursued /completed the internship will be declared as failed and have to complete during subsequent SEE examination after they satisfy the internship requirements.

CMEP: Cost Management of Engg Projects, OSHA: Occupational Safety and Health Administration

Sl. No	Name	Signature	Sl. No	Name	Signature
1	Dr. Jyoti P Koujalagi		11	Mr. Samarendra Prathap Singh	
2	Dr. Suryanarayana doolla		12	Dr. B.V. Sumangala,	
3	Dr. Satish L		13	Dr. Shankarlingappa.C.B	
4	Dr. Pradip Kumar Dixit		14	Dr. Eranna, Asso. Prof.	
5	Dr. Puttaswamy PS		15	Mr. Dayananda T.B,	
6	Dr. Sumathi, RNSIT		16	Dr. Govindaraju H.V,	
7	Mr. Ramachandra,		17	Dr. Vasudevamurthy S,	
8	Mr. Arun Kumar		18	Ms. Arpitha Raju	
9	Mr. Guruswamy		19	Mrs. Harini Vaikund	
10	Mr. Srinivas BT		20	Mr. V keshavmurthy	

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
 SCHEME OF TEACHING AND EXAMINATION for Batch: 2019, Academic Year: 2020-21
 B.E Name of the programme: Electrical & Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

III SEMESTER

Course and Course Code	Course Title	Teaching Department	Teaching Hours /Week				Examination				Credits
			Theory Lecture	Tutorial	Practical/ Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks		
			L	T	P						
BC 18MA31	Transforms & Applications	Maths	2	2	--	03	50	50	100	3	
PC 18EE31	Analog Electronic Circuits		4	--	--	03	50	50	100	4	
PC 18EE32	Logic Design		3	0	--	03	50	50	100	3	
PC 18EE33	Network Analysis		3	2	--	03	50	50	100	4	
PC 18EE34	Transformer and Induction Machines		4	0	--	03	50	50	100	4	
PC 18EE35	Generation, Transmission & Distribution		3	0	--	03	50	50	100	3	
PC 18EEL36	Electronic Circuits Lab		--		2	03	50	50	100	1	
PC 18EEL37	Logic Design Lab		--		2	03	50	50	100	1	
HS 18HS31/32	Constitution of India Professional Ethics and Human Rights/ / Env. Studies	Hu/Civ	1	--	--	02	50	50	100	1	
MC 18HS33	Soft skills (MC)	Humanities	04		--	03	50	-	50	0	
TOTAL			24	04	04	29	500	450	950	24	
Course prescribed to lateral entry Diploma holders admitted to III semester of Engineering programs											
MC 18MAD31	Advance Mathematics - I	Mathematics	02	02	--	03	50	00	50	00	

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
 SCHEME OF TEACHING AND EXAMINATION for Batch: 2019, Academic Year: 2020-21
 B.E Name of the programme: Electrical & Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

IV SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				
					Theory Lecture	Tutorial	Practical	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	BC	18MA41	Probability, Numerical and Optimization Techniques	MA	2	2	--	03	50	0	100	
2	PC	18EE41	Electrical & Electronic Measurements and Instruments		3	0	--	03	50	50	100	
3	PC	18EE 42	Control Systems		4	--	--	03	50	50	100	
4	PC	18EE 43	DC Machines and Synchronous Machines		4	0	--	03	50	50	100	
5	PC	18EE 44	Power Electronics		4	0	--	03	50	50	100	
6	PC	18EE 45	Linear Integrated Circuits & Applications		4	0	--	03	50	50	100	
7	PC	18EE L46	Transformer and Induction Machines Lab		0		2	03	50	50	100	
8	PC	18EE L47	Power Electronics Lab		0		2	03	50	50	100	
9	HS	18HS41/42	Constitution of India Professional Ethics and Human Rights/ Env. Studies	HS/CV	1	--	--	02	50	50	100	
10	MC	18HS43	Soft skills (NCCM)	HS	04		--	03	50	-	50	
TOTAL					24	04	04	29	500	450	950	24
Course prescribed to lateral entry Diploma holders admitted to III semester of Engineering programs												
11	MC	18MAD41	Advance Mathematics - II	MA	02	02	--	03	50	00	50	0

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION for 2018 batch - Academic Year 2020-21
 B.E in Electrical and Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)
V Semester

Sl. No	Course and Course Code		Course Title	Teaching Dept.	Teaching Hours / Week			Examination			Credits	
					Theory Lecture (L)	Tutorial (T)	Practical (P)	Duration in Hours	CIE Marks	SEE Marks		Total Marks
1	HS	18HS51	Management and Entrepreneurship	HS	03	00	00	03	050	050	100	03
2	PC	18EE51	Signals and Systems	EE	02	02	00	03	050	050	100	03
3	PC	18EE52	Field Theory	EE	04	00	00	03	050	050	100	04
4	PC	18EE53	Microcontroller	EE	03	00	00	03	050	050	100	03
5	PC	18EE54	Electrical Machine Design	EE	04	00	00	03	050	050	100	04
6	PE	18EE55x	Professional Elective-I	EE	03	00	00	03	050	050	100	03
7	PE	18EE56x 18EE01	Open Elective-A	EE	03	00	00	03	050	050	100	03
8	PC	18EEL57	Control Systems Lab	EE	00	00	02	03	050	050	100	01
9	PC	18EEL58	DC Machines and Synchronous Lab	EE	00	00	02	03	050	050	100	01
Total					22	02	04	27	450	450	900	25

Mini-project: To be carried out during the intervening vacations of V and VI semesters. The SEE examination will be conducted during VI semester. The credit prescribed for mini – project is added to VI semester credits. The mini-project is considered as a head of passing and is considered for the award of degree. Those, who do not take-up/complete the mini-project will be declared as failed and have to complete during subsequent SEE examination after satisfy the Mini-project requirements. Also, mini-project is considered for eligibility to VII semester.

Note: BC: Science Course, PC: Professional Core, HS: Humanities

Professional Elective-I(03 Credits)		
Sl. No	Course Code	Course Title
1	18EE551	Programmable Logic Controllers
2	18EE552	VLSI Circuits & Design

Professional Elective-I(03 credits)		
Sl. No	Course Code	Course Title
3	18EE553	Modern Control Theory
4	18EE554	Embedded Systems

Open Elective-A(OE-A)		
Sl. No	Course Code	Course Title
1	18EEE01	Renewable Energy Sources

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION for 2018 batch - Academic Year 2020-21
 B.E in Electrical and Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)

VI Semester

S I N O	Course and Course Code		Course Title	Teaching Dept.	Teaching Hours / Week			Examination				Cre dits
					Theory Lecture (L)	Tutorial (T)	Practical (P)	Duration in Hours	CIE Marks	SEE Marks	Total Marks	
1	H S	18HS61	Intellectual Property Rights(IPR)	HS	02	00	00	02	050	050	100	03
2	P C	18EE61	Power System Analysis-I	EE	04	00	00	03	050	050	100	04
3	P C	18EE62	High Voltage Engineering	EE	03	00	00	03	050	050	100	03
4	P C	18EE63	Digital Signal Processing	EE	03	02	00	03	050	050	100	04
5	P E	18EE64X	Professional Elective-2	EE	03	00	00	03	050	050	100	03
6	O E	\$ 18EE65x	Open Elective-A	EE	03	00	00	03	050	050	100	03
7	P C	18EEL656	Digital Signal Processing Lab	EE	00	00	02	03	050	050	100	01
8	P C	18EEL67	Micro controller Lab	EE	00	00	02	03	050	050	100	01
9	M P	18EEMP68	Mini-Project	EE	00	00	00	03	050	050	100	02
10	I N T	18EEI689	Internship		00	00	00	00	000	000	000	00
<i>Total</i>					18	02	04	26	450	450	900	24

a) **Internship:** All the students admitted to III year of BE have to undergo mandatory internship of 4 weeks during the vacations of VI and VII semesters and /or VII and VIII semesters. A University examination will be conducted during VIII semester and prescribed credit are added to VIII semester. Internship is considered as a head of passing and is considered for the award of degree. Those, who do not take-up/complete the internship will be declared as failed and have to complete during subsequent University examination after satisfy the internship requirements

Note: BC: Science Course, PC: Professional Core, HS: Humanities, NCMC: Non-Credit Mandatory Course
 S-Elective code of the Department offering the course, A-Students shall register for a course offered by the other departments

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION from Academic Year 2020-21
 B.E in Electrical and Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)(Applicable to 2017 Batch)
VII Semester

S I. N o	Course and Course Code		Course Title	Teach ing Dept.	Teaching Hours / Week			Examination				Cred its
					Theor y Lectu re (L)	Tutor ial (T)	Practic al (P)	Duratio n in Hours	CIE Mar ks	SEE Mar ks	Tota l Mar ks	
1	M C	HS04	Intellectual Property Rights	IM	02	00	00	03	025	025	050	02
2	P C	EE71	Computer Techniques in Power System Analysis	EE	03	02	00	03	050	050	100	04
3	P C	EE72	High Voltage Engineering	EE	04	00	00	03	050	050	100	04
4	P E	EE73X	Elective – E	EE	04	00	00	03	050	050	100	04
5	P E	\$	Inter department Elective *	EE	00	00	00	03	050	050	100	04
6	O E	EEL74	Relay & HV Lab	EE	00	00	00	03	050	050	100	1.5
7	P C	EEL75	Power Systems Simulation Laboratory	EE	00	00	02	03	050	050	100	1.5
8	P C	EED76	Computer Aided Electrical Drawing	EE	02	00	02	03	050	050	100	03
9	P R J	EEP77	Project Work Phase-I	EE	00	00	02	03	050	050	050	00
<i>Total</i>					15	02	06	27	425	375	800	24

\$-Inter departmental Electives: Students who have not completed the IDE should register for the completion of 200 Credits. According to section 16.2 academic regulations of Dr. AIT, the credits registered should not exceed 20.

Students shall register for 1 course in the Elective Group-E; \$- Elective core of the D

Elective-E		
Sl. No	Course Code	Course Title
1.	EE731	Flexible AC Transmission Systems(FACTS)
2.	EE732	Energy Auditing & Demand Side Management
3.	EE733	Power Systems Dynamics & Stability

Professional Elective-4(PE-4)		
Sl. No	Course Code	Course Title
1	EE734	Fuzzy Logic
2	EE735	Artificial Neural Network
3	EE736	Electrical Power Quality

Open Elective-B(OE-B)		
Sl. No	Course Code	Course Title
1	EEE01	Renewable Energy Sources
2	EEE02	Advanced Power Electronics

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION from Academic Year 2020-21
 B.E in Electrical and Electronics Engineering
 Outcome Based Education (OBE) and Choice Based Credit System (CBCS)(Applicable to 2017 Batch)
VIII Semester

Sl. No	Course and Course Code		Course Title	Teaching Dept.	Teaching Hours / Week			Examination				Credits
					Theory Lecture (L)	Tutorial (T)	Practical (P)	Duration in Hours	CI E Marks	SEE Marks	Total Marks	
1	PC	EE81	Modern Power System Protection	EE	04	00	00	03	050	050	100	04
2	PC	EE82	Power System Operation & Control	EE	03	00	00	03	050	050	100	03
3	PE	EE83x	Elective - F	EE	04	00	00	03	050	050	100	04
4	PRJ	EEP84	Project Work Phase-II	EE	00	00	09	03	100	200	300	12
5	Seminar	EES85	Seminar/ Project Tour/Industrial Visit	EE	00	00	00	02	050	000	050	02
Total					011	00	09	14	300	350	650	25

Note : Inter departmental Electives: Students who have not completed the IDE should register for the completion of 200 Credits. According to section 16.2 academic regulations of Dr. AIT, the credits registered should not exceed 20
 Students shall register for 1 course in the Elective Group-F; S- Elective core of the D

Elective-F(04 Credits)		
Sl. No	Course Code	Course Title
1.	EE831	Testing & Commissioning of Electrical Equipment
2.	EE832	HVDC Transmission
3.	EE833	Insulation Engineering

Elective-F(04 Credits)		
Sl. No	Course Code	Course Title
1	EE834	Artificial Intelligence Applications to Power Systems
2	EE835	Computer Control of Electrical Drives
3	EE836	Micro & Smart System Technology

Open Elective-B(OE-B)		
Sl. No	Course Code	Course Title
1	EEE03	Advanced Control Systems
2	EEE04	Electromagnetic Compatibility



DR. AMBEDKAR INSTITUTE OF TECHNOLOGY, BENGALURU
(An Autonomous Institution Affiliated to VTU, Belgaum)

DEPARTMENT OF ELECTRICAL AND ELECTRONICS ENGINEERING

LIST OF BOARD OF STUDIES EEE MEMBERS FOR THE YEAR 2019-2021

Sl No.	CATEGORY	Nomination of the Committee	Name of the Persons
1	Head of the Department	Chairperson	Dr.G.V.Jayaramaiah, Professor
2	Subject External Experts from outside the College Nominated by Academic Council	Members	1 Dr. Suryanarayanadoolla, IIT Mumbai
			2 Dr. Satish IISc, Bengaluru
			3 Dr. Pradeep Kumar Dixit,MSRIT, Bengaluru-19
			4 Dr. P S Puttaswamy-PESCOE, Mandya
3	Expert from outside College, Nominated by Vice-Chancellor (VTU Nominee)	Member	1 Dr. Sumathi, RNSIT, Bengaluru
4	Representative from Industry/Corporate Sector/Allied area related to Placement Nominated by Academic council	Members	1 Mr. Ramachandra, Green Automation Pvt. Ltd, Bengaluru
			2 Mr. Arun Kumar, Electrono Solution, Pvt.ltd, Bengaluru
			3 Mr.Guruswamy,General Manager, IE Power Technologies, Bengaluru
5	Post Graduate Meritorious alumnus nominated by Principal	Member	1 Mr. Srinivas BT, Software Engineer, IBM India Pvt. Ltd, Bengaluru
6	Mentee Institution Member	Member	1 Mr. Dilip Kumar, Prof and Head, Dept of EEE, IET Ayodhya, UP, India
7	Faculty Members at Different Levels Bearing Different Specializations	Members	1 Dr. Eranna, Asso. Prof.
			2 Dr. Jyoti P Koujalagi, Professor
			3 Mr. Dayananda T.B, Asso. Prof.
			4 Dr. Govindaraju H.V, Asso. Prof.
			5 Dr. Vasudevamurthy S, Asso. Prof.
			6 Ms. Nalini S, Asso. Prof.
			7 Dr.Shankarlingappa.C.B, Professor
			8 Ms. Arpitha Raju
Co-opted Members			1 Mr. MukundaSwamy, Asst. Prof.
			2 Mrs. HariniVaikund, Asst. Prof.
			3 Mrs.Dhanyavathi., Asst. Prof.
			4 Mrs. Pankaja.G, Asst. Prof.
			5 Mrs. DeeptiS.S, Asst. Prof.
			6 Mr. Rajesh LV, Asst. Prof.

Jayaramaiah

Chair Person
BOS EEE

Ref. No. DR.A/EE/EI 203/14-9-21

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
 SCHEME OF TEACHING AND EXAMINATION from Academic Year 2021-22
 B.E in Electrical and Electronics Engineering

Outcome Based Education (OBE) and Choice Based Credit System (CBCS) (Applicable to 2019-22 Batch)

V SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical/ Drawing	Duration in	CIE Marks	SEE Marks	Total Marks	
1	HS	18HS51/52	Management and Entrepreneurship/IPR	Hu	3	-	--	03	50	50	100	3
2	PC	18EE51	Signals and Systems	EE	2	2	--	03	50	50	100	3
3	PC	18EE52	Field Theory		4	--	--	03	50	50	100	4
4	PC	18EE53	Microcontroller		2	2	--	03	50	50	100	3
5	PC	18EE54	Electrical Machine Design		4	--	--	03	50	50	100	4
6	PE	18EE55X	Elective - 1		3	--	--	03	50	50	100	3
7	OE	*18XX56 X	Open Elective - A		3	--	--	03	50	50	100	3
8	PC	18EEL57	Control Systems Lab		--	--	2	03	50	50	100	1
9	PC	18EEL58	DC Machines and Synchronous Lab		--	--	2	03	50	50	100	1
TOTAL					21	04	4	27	450	450	900	25

10	HS	18HS55	Placement Training	Hu	02	--	--	03	50	-	50	PP/NP
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Note: Hu: Humanities, PC: Professional Core, MC: Mandatory Course

*keep the course code as it is and it shall be replaced with department code depending on the student's choice.

Electives

Course code	Professional Electives - I	Open Elective - A
8EE551	Programmable Logic Controllers	Students can select any one of the open electives (Please refer to consolidated list of Dr AIT for open electives) offered by any Department. Selection of an open elective is not allowed provided: <ul style="list-style-type: none"> The candidate has studied the same course during the previous semesters of the programme. The syllabus content of open elective is similar to that of Departmental core courses or professional electives. A similar course, under any category, is prescribed in the higher semesters of the programme. Registration to electives shall be documented under the guidance of Programme Coordinator/ Mentor.
8EE552	VLSI Circuits & Design	
8EE553	Modern Control Theory	
8EE554	Embedded Systems	
	OPEN ELECTIVE-A	
8EE561	Renewable Energy Sources	

[Signature]
 HOD 14/9/21

Department of Electrical and Electronics Engg.
 Dr. Ambedkar Institute of Technology
 Bengaluru-560056

Received
 (Dean (a))
[Signature] 14/9.

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION from Academic Year 2021-22

B.E in Electrical and Electronics Engineering
Outcome Based Education(OBE) and Choice Based Credit System (CBCS) (Applicable to 2019-22 Batch)

VI SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits	
					Theory Lecture	Tutorial	Practical / Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks		
													L
1	HS	18HS61/62	M & E /IPR	Hu	3	--	--	03	50	50	100	3	
2	PC	18EE61	Power System Analysis-I	EE	2	2	--	03	50	50	100	3	
3	PC	18EE62	High Voltage Engineering	EE	3	2	--	03	50	50	100	4	
4	PC	18EE63	Digital Signal Processing	EE	3	2	--	03	50	50	100	4	
5	PE	18EE4X	Professional Elective -2	EE	3	--	--	03	50	50	100	3	
6	OE	*18XX65X	Open Elective -B		3	--	--	03	50	50	100	3	
7	PC	18EEL66	Digital Signal Processing Lab	EE	--	--	2	03	50	50	100	1	
8	PC	18EEL67	Micro controller Lab	EE	--	--	2	03	50	50	100	1	
	MP	18EEM68	Mini-project					03	50	50	100	2	
0	INT	18EEI69	Industry Internship	(To be carried out during the intervening vacations of VI and VII semesters)				--	--	--	--	--	--
TOTAL					17	6	4	27	450	450	900	24	

HS	18HS66	Placement Training	Hu	02	--	--	03	50	-	50	PP/NP
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Note: PC: Professional core, PE: Professional Elective, OE: Open Elective, MP: Mini-Project, INT: Internship.
 *keep the course code as it is and it shall be replaced with department code depending on the student's choice.

Internship: All the students admitted to III year of BE have to undergo mandatory internship of 4 weeks during the vacations of VI and VII semesters and /or VII and VIII semesters. A University examination will be conducted during VIII semester and prescribed credit are added to VIII semester. Internship is considered as a head of passing and is considered for the award of degree. Those, who do not take-up/complete the internship will be declared as failed and have to complete during subsequent University examination after satisfy the internship requirements.


Electives	
Professional Electives -2	
EE641	Operating System
EE642	Special Electrical Machines
EE643	Artificial Intelligence to Electrical Engineering
EE644	Electrical Vehicle Technology
EE645	Smart Grid Technology
EE646	OOPS with C++
Open Elective -B	
EE651	Electrical Vehicle Technology

Open Elective -B

Students can select any one of the open electives (Please refer to consolidated list of Dr AIT for open electives) offered by any Department.

- Selection of an open elective is not allowed provided, The candidate has studied the same course during the previous semesters of the programme.
- The syllabus content of open elective is similar to that of Departmental core courses or professional electives.
- A similar course, under any category, is prescribed in the higher semesters of the programme.

Registration to electives shall be documented under the guidance of Programme Coordinator/ Mentor.


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 Department of Electrical and Electronics Engg.
 Dr. Ambedkar Institute of Technology
 Bengaluru-560056

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION from Academic Year 2021-22
B.E in Electrical and Electronics Engineering

Outcome Based Education(OBE) and Choice Based Credit System (CBCS) (Applicable to 2018-21 Batch)

VII SEMESTER

Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical / Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
1	MC	18HS71	CMEP /OSHA	IM/ CV	2	--	--	03	50	50	100	2
2	PC	18EE71	Power System Analysis-2	EE	4	--	--	03	50	50	100	3
3	PC	18EE72	Modern Power System Protection	EE	4	--	--	03	50	50	100	4
4	PE	18EE73X	Professional Elective -3	EE	3	--	--	03	50	50	100	3
5	PE	18EE74X	Professional Elective -4	EE	3	--	--	03	50	50	100	3
6	OE	*18XX75X	Open Elective -C		3	--	--	03	50	50	100	3
7	PC	18EEL76	Power System Simulation Lab	EE	--	--	2	03	50	50	100	1
8	PC	18EEL77	Relay & High Voltage Lab	EE	--	--	2	03	50	50	100	1
9	PC	18EEL78	Computer Aided Electrical Drawing	EE			2	03	50	50	100	1
10	Project	18EEP79	Project Work Phase - 1	EE	--	--	2	03	50	50	100	2
11	INT	18EEI79	Internship	(If not completed after VI semester examinations, it has to be carried out during the intervening vacations of VII and VIII semesters)				--	--	--	--	--
TOTAL					19	--	8	30	500	500	1000	23

Note: PC: Professional Core, PE: Professional Elective, OE: Open Elective, INT: Internship, MC: Mandatory Course
 *keep the course code as it is and it shall be replaced with department code depending on the student's choice

Internship: All the students admitted to III year of BE have to undergo mandatory internship of 4 weeks during the vacations of VI and VII semesters and /or VII and VIII semesters. A SEE examination will be conducted during VIII semester and prescribed credits shall be added to VIII semester. Internship is considered as a head of passing and is considered for the award of degree. Those, who do not take-up/complete the internship will be declared as failed and have to complete during subsequent SEE examination after satisfy the internship requirements.

Electives

Course code	Professional Electives - 3	Open Elective =C
18EE731	Sensors and Transducers	Students can select any one of the open electives (Please refer to consolidated list of Dr. AIT for open electives) offered by any Department. Selection of an open elective is not allowed provided, <ul style="list-style-type: none"> The candidate has studied the same course during the previous semesters of the programme. The syllabus content of open elective is similar to that of Departmental core courses or professional electives. A similar course, under any category, is prescribed in the higher semesters of the programme. Registration to electives shall be documented under the guidance of Programme Coordinator/ Mentor.
18EE732	Insulation Engineering	
18EE733	Flexible AC Transmission Systems (FACTS)	
18EE741	Professional Electives - 4	
18EE742	Power System Operation & Control	
18EE743	Computer Control of Electrical Drives	
	Energy Auditing & Demand Side Management	
18EE751	Open Elective -C	
	Energy Auditing & Demand Side Management	

CMEP: Cost Management of Engg. Projects,

OSHA: Occupational Safety and Health Administration

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 Dr. Ambedkar Institute of Technology

Dr. Ambedkar Institute of Technology, Bengaluru-560 056
SCHEME OF TEACHING AND EXAMINATION from Academic Year 2021-22
B.E Name of the programme
Outcome Based Education(OBE) and Choice Based Credit System (CBCS) (Applicable to 2018-21 Batch)

VIII SEMESTER


Sl. No	Course and Course code		Course Title	Teaching Department	Teaching Hours /Week			Examination				Credits
					Theory Lecture	Tutorial	Practical / Drawing	Duration in hours	CIE Marks	SEE Marks	Total Marks	
					L	T	P					
1	MC	18XX81	CMEP /OSHA	IM /CV	4	--	--	03	50	50	100	02
2	Project	18EEP81	Project Work Phase - 2		--	--	2	03	50	50	100	10
3	Seminar	18EES82	Technical Seminar		--	--	2	03	50	50	100	01
	INT	18EEI83	Internship	(Completed during the intervening vacations of VI and VII semesters and /or VII and VIII semesters.)				03	50	50	100	02
TOTAL					04	--	4	12	200	200	400	15

Note: PC: Professional Core, PE: Professional Elective, OE: Open Elective, INT: Internship, MC: Mandatory Course

Electives

Internship: Those, who have not pursued /completed the internship, will be declared as failed and have to complete during subsequent SEE examination after they satisfy the internship requirements.

CMEP: Cost Management of Engg Projects, OSHA:Occupational Safety and Health Administration


 Department of Electrical and Electronics Engg.
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